

Electronic Acknowledgement Receipt

EFS ID:	1177708
Application Number:	10699975
Confirmation Number:	5032
Title of Invention:	Mask layer and interconnect structure for dual damascene semiconductor manufacturing
First Named Inventor:	Isalah O. Oladeji
Customer Number:	47396
Filer:	Charles W. Gaines/Karen Vertz
Filer Authorized By:	Charles W. Gaines
Attorney Docket Number:	JESSEN 7-1-4
Receipt Date:	30-AUG-2006
Filing Date:	02-NOV-2003
Time Stamp:	11:00:04
Application Type:	Utility
International Application Number:	

Payment information:

Submitted with Payment	no
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part	Pages
1	Supplemental Appeal Brief	CorrectedAppealBrief.pdf	1510912	no	31

Warnings:**Information:****Total Files Size (in bytes):**

1510912

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New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.